

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	7327270
<b>Application Number:</b>	10595303
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	7114
<b>Title of Invention:</b>	Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers
<b>First Named Inventor/Applicant Name:</b>	Roy Knechtel
<b>Customer Number:</b>	21967
<b>Filer:</b>	Jeff Vockrodt/Marcus Cunningham
<b>Filer Authorized By:</b>	Jeff Vockrodt
<b>Attorney Docket Number:</b>	60291.000048
<b>Receipt Date:</b>	31-MAR-2010
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<b>Application Type:</b>	U.S. National Stage under 35 USC 371

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		IDS_60291_000048.pdf	74764 dde42516294d542a8aca77aefb1ee3d98adcbcc5	yes	3

	<b>Multipart Description/PDF files in .zip description</b>								
	<b>Document Description</b>		<b>Start</b>	<b>End</b>					
	Transmittal Letter		1	2					
	Information Disclosure Statement (IDS) Filed (SB/08)		3	3					
<b>Warnings:</b>									
<b>Information:</b>									
2	Foreign Reference	JP09219423.pdf	232046	no	6				
			2e30374dff0ab5df10ba8c625dfb07b07ee0a2ef						
<b>Warnings:</b>									
<b>Information:</b>									
3	Foreign Reference	JP2000114409.pdf	485535	no	8				
			6ea26450e9399efa313c5e9a7af653b3d857c18b						
<b>Warnings:</b>									
<b>Information:</b>									
<b>Total Files Size (in bytes):</b>			792345						
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